

EV832836763

Inventor: **Todd R. Abbott et al.**

Title: **Methods of Forming Semiconductor Structures**

Assignee: **Micron Technology, Inc.**

Serial No.: **10/800,196**

Filed: **March 11, 2004 [RCE filed herewith]**

Our Ref.: **MI22-2447**

**INFORMATION DISCLOSURE STATEMENT**


**PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98**

The attached Form PTO-1449 is submitted in compliance with 37 CFR §1.56. Pursuant to FEDERAL REGISTER, Vol. 69, No. 182, pg. 56542 (September 21, 2004), no copies of any cited U.S. patents or U.S. published applications are included herewith. Copies of all other cited art are attached. No admission is made regarding whether the listed references are prior art.

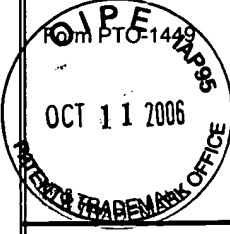
Citation of these references is respectfully requested.

Respectfully submitted,

Dated: 10/11/06

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		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-2447		SERIAL NO. 10/800,196	
		LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)					
		APPLICANT: Todd R. Abbott et al.					
FILING DATE Mar. 11, 2004 [RCE filed herewith]						GROUP 2813	

U.S. PATENT DOCUMENTS							
*Examiner's Initials	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
AA	5,574,299	11/96	Kim				
AB	6,159,789	12/00	Chuang et al.				
AC							
AD							
AE							
AF							
AG							
AH							

FOREIGN PATENT DOCUMENTS							
Document Number	Date	Country	Class	Subclass	Translation		
					Yes	No	
AI	US2005/006211	02/05	PCT Search Report				
AJ							
AK							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)			
	AL		
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EXAMINER	DATE CONSIDERED
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.